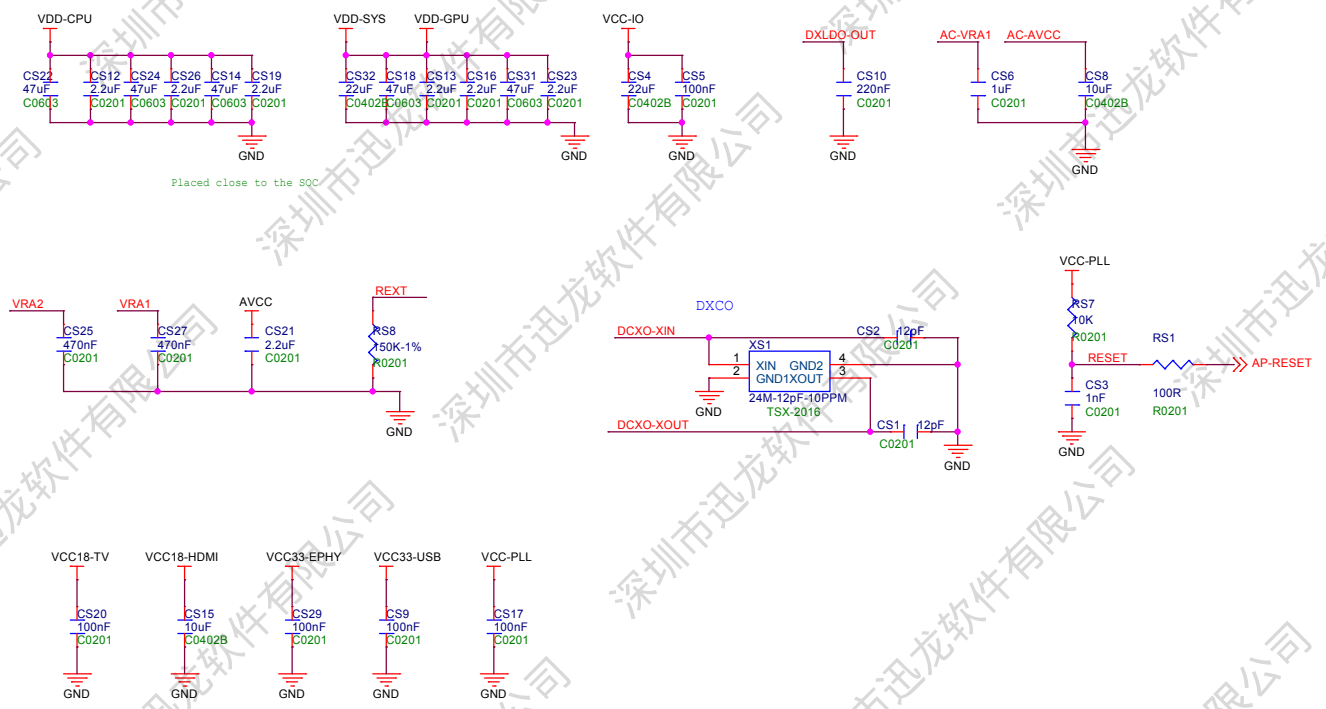


JTAG-B 0: FORCE JTAG TO PG  
1: SWITCH JTAG TO PG OR PF. DEFAULT PF.

H616-BGA-284

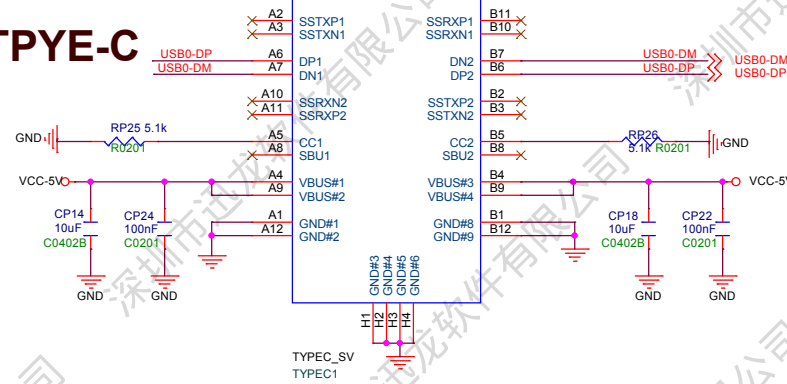


Placed close to the SOC



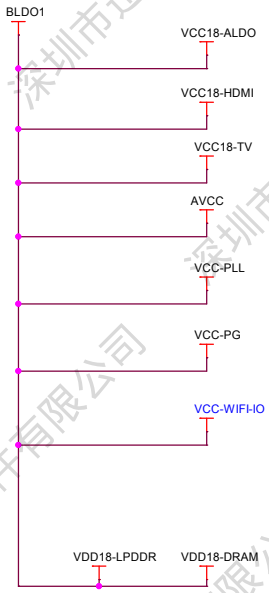


# TPYE-C

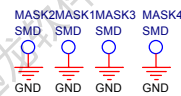
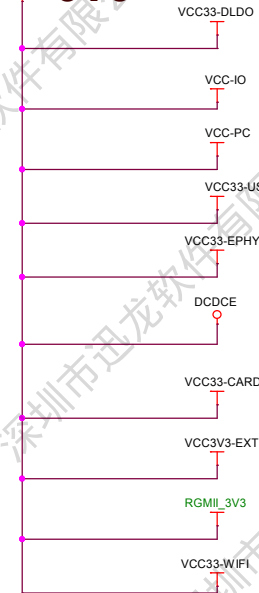


TYPEC\_SV  
TYPEC1

# 1V8

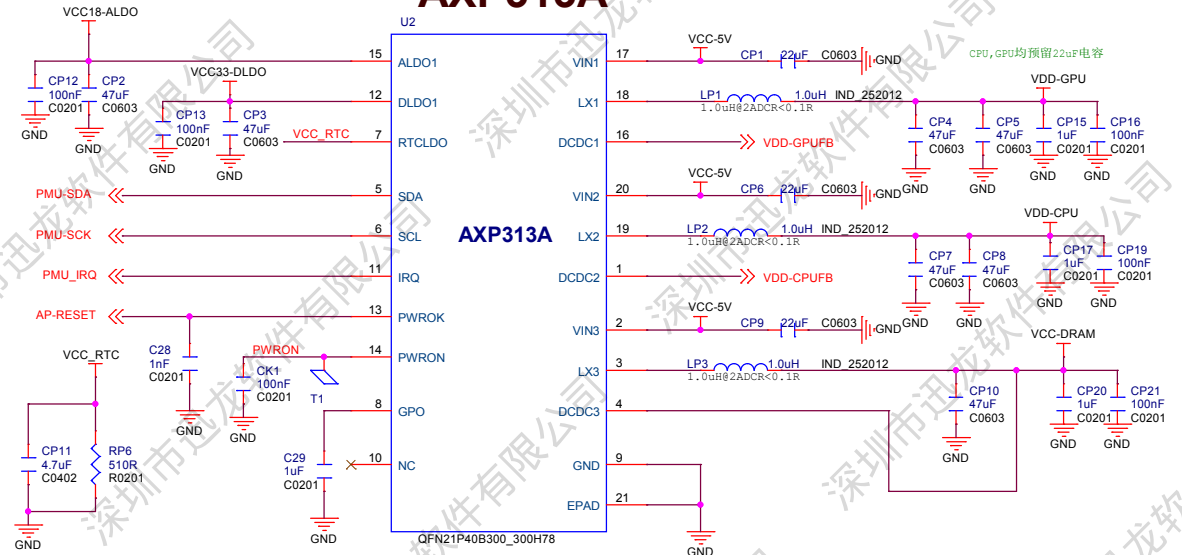


# 3V3

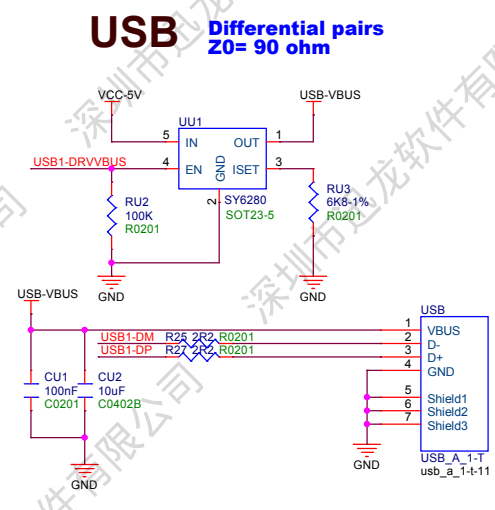
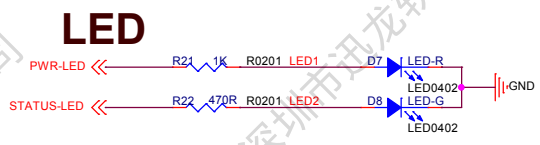
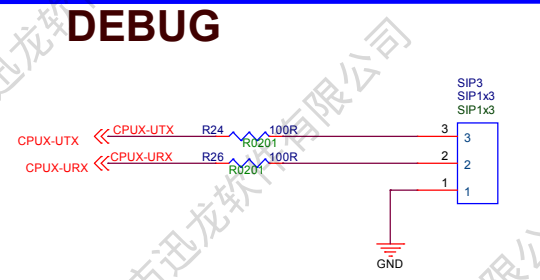
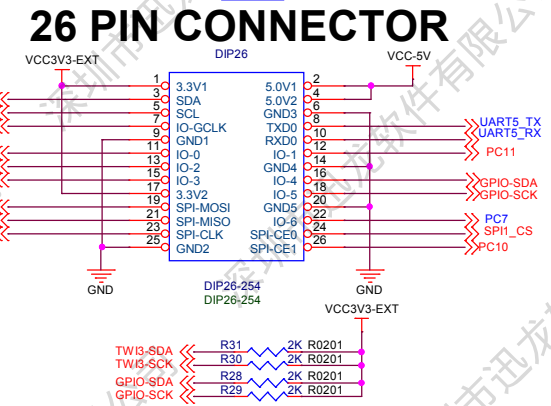
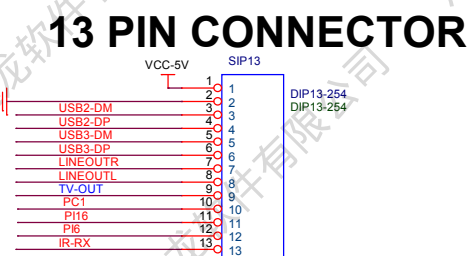
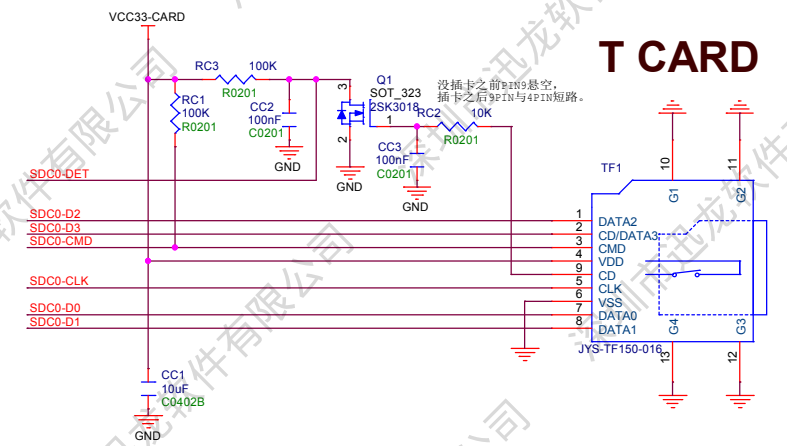
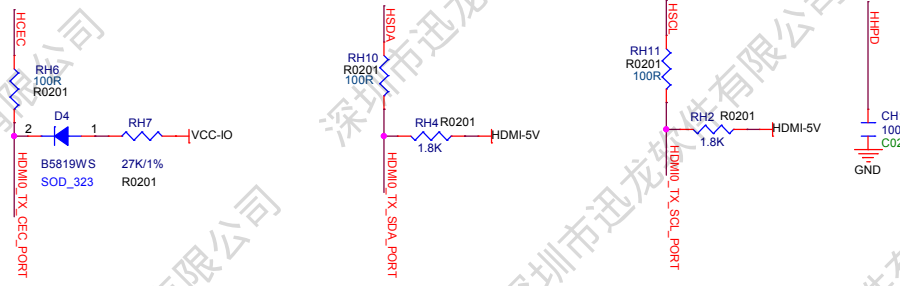
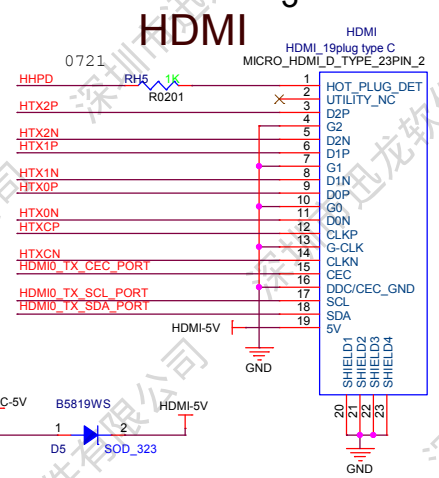
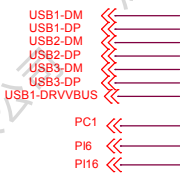
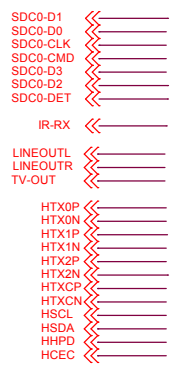


# PMU

# AXP313A



Xunlong Software Co., Limited		
Design Name		
ORANGEPI Zero3-4G		
Size	Page Name	Rev
A3	POWER	1.2
Date:	Thursday, July 13, 2023	Sheet 4 of 7



# 1000M ETH

Differential pairs  
Z0= 100 ohm

RGMII0\_CLK125 ← RGMII0\_CLK125 ← CLK125

RGMII0\_MDIO R8 1 R2021 2 1.5K RGMII0\_3V3

5	RGMII0_RXC	<OUT	0_RXC
5	RGMII0_RXCTL	<OUT	0_RXCTL
5	RGMII0_RXD0	<OUT	0_RXD0
5	RGMII0_RXD1	<OUT	0_RXD1
5	RGMII0_RXD2	<OUT	0_RXD2
5	RGMII0_RXD3	<OUT	0_RXD3
5	RGMII0_TXC	<IN	0_TXC
5	RGMII0_TXCTL	<IN	0_TXCTL
5	RGMII0_TXD0	<IN	0_TXD0
5	RGMII0_TXD1	<IN	0_TXD1
5	RGMII0_TXD2	<IN	0_TXD2
5	RGMII0_TXD3	<IN	0_TXD3

NAT0_MDIOp	1	MDI[0]+
NAT0_MDIOm	2	MDI[0]-
0_AVDD10	3	AVDD10_3
NAT0_MDIO1p	4	MDI[1]+
NAT0_MDIO1m	5	MDI[1]-
NAT0_MDIO2p	6	MDI[2]+
0_AVDD10	7	MDI[2]-
NAT0_MDIO3p	9	MDI[3]+
NAT0_MDIO3m	10	MDI[3]-

MAX:1A  
YT8531C

U54

